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I am a PhD student in Electrical Engineering and I began my research in Circuit Design for the Front-End Electronics of Detectors at the Instrumentation division in BNL in September 2015. Before this, I worked as a VLSI Physical Design Engineer at IBM, India for 3 years and did my Bachelor's degree in Electrical Engineering from India.

I feel that ASAP is an interesting cohort that helps in making new friends from various STEM fields and collaborating with them to organize various social events. Being a part of the board I hope to work with the team to keep up the good work done by my peers to develop a sense of community at BNL.