TIME: 8:00AM PDT / 11:00AM EDT / 5:00PM Munich

ModSim 2020 – 1 Day Virtual Workshop, August 12, 2020	
8:00-8:15a.m. PDT	Opening Remarks – Adolfy Hoisie
8:15-9:00a.m. PDT	Keynote Speaker **Margaret Martonosi** – **Computer Systems Research and Design in the Post-Moore Era: Navigating the Seismic Shift**
9:05-10:05a.m. PDT	*Panel:* ***Is ML the Holy Grail of ModSim*** *Moderator:* ***Shekhar*** **Borkar** *Panelist:* **Bruce Jacob, Serge Leef, Subhasish Mitra, Rob Schreiber**
Contributed Presentations Session: – RAPID-FIRE : Session Lead: Martin Schulz	
10:10-10:20a.m. PDT	Abhinav Bhatele – **Machine Learning to Model Performance Variability on HPC Systems**
10:20-10:30a.m. PDT	Subhankar Pal – **HetSim: Simulating Large-Scale Heterogeneous Systems using a Trace-driven, Synchronization and Dependency-Aware Framework**
10:30-10:40a.m. PDT	Gokul Subramanian Ravi – **Coordinated Design of Workloads and Systems via Machine Learning**
10:40-10:50a.m. PDT	Jeremiah Wilke – **Putting compilers in the simulation co-design loop with surrogate performance models**
10:50-11:00a.m. PDT	Ayaz Akram – **gem5art: Artifact, Reproducibility and Testing Framework for gem5**
11:00-11:10a.m. PDT	Ananda Samajdar – **SCALE-Sim: Systolic CNN accelerator simulator**
11:10-11:20a.m. PDT	Bogil Kim – **Nebula: Lightweight Neural Network Benchmarks**
11:20-11:30a.m. PDT	Arun Sathanur – **Learning CPU Performance Bottleneck Diagnosis**
11:30-11:40a.m. PDT	Simon McIntosh-Smith – **Modelling Advanced Arm-based CPUs with SimEng**
11:40-11:50a.m. PDT	Aravind Neelakantan– **System-level MODSIM of CiM Architectures for Memory-Intensive Applications**
11:50am-12:00p.m. PDT	Lingda Li – **Machine Learning-based Detailed Microarchitecture Simulation**
12:00-12:10p.m. PDT	Willie Lim – **Kiva - A Cycle Accurate Simulator for HPC**
12:10-12:20p.m. PDT	Thomas Flynn – **Impact of training set characteristics on machine learning-driven latency simulation**
12:20-12:30 p.m. PDT	Jason Liu – **Memory Architecture Performance Optimization via Modeling and Simulation**
12:30-12:40 p.m. PDT	Kuba Kaszyk – **Full-System GPU Design Space Exploration**
12:40-12:50 p.m. PDT	**Closing Remarks – Adolfy Hoisie**
	End of Abbreviated Workshop