## **T. HOEFLER**

## The Three Pillars of Large-scale Deep Learning

with contributions by the whole SPCL deep learning team (T. Ben-Nun, S. Li, N. Dryden and many others) and collaborators (D. Alistarh and others)



## Pushing the envelope in large-scale learning





## **High-Performance I/O**

- Quickly growing data volumes
  - Scientific computing!
- Use the specifics of machine learning workloads
  - E.g., intelligent prefetching

**CLAIRVOYANT PREFETCHING FOR DISTRIBUTED MACHINE LEARNING I/O** 

Roman Böhringer Nikoli Dryden<sup>1</sup> Tal Ben-Nun<sup>1</sup> Torsten Hoefle

### ABSTRACT

I/O is emerging as a major bottleneck for machine learning training, especially in distributed environments such as clouds and supercomputers. Optimal data ingestion pipelines differ between systems, and increasing efficiency requires a delicate balance between access to local storage, external filesystems, and remote workers; yet existing frameworks fail to efficiently utilize such resources. We observe that, given the seed generating the random access pattern for training with SGD, we have clairvoyance and can exactly predict when a given sample will be accessed. We combine this with a theoretical analysis of access patterns in training and performance modeling to produce a novel machine learning I/O middleware, HDMLP, to tackle the I/O bottleneck. HDMLP provides an easy-to-use, flexible, and scalable solution that delivers better performance than state-of-the-art approaches while requiring very few changes to existing codebases and supporting a broad range of environments

### **High-Performance Compute**

- Deep learning is HPC
  - Same major problems
  - Data movement!

#### Data Movement Is All You Need: A Case Study on **Optimizing Transformers** Andrei Ivanov\*, Nikoli Dryden\*, Tal Ben-Nun, Shigang Li, Torsten Hoefler ETH Zürich firstname.lastname@inf.ethz.ch Equal contribution Abstract-Transformers have become widely used for language challenges such as artificial general intelligence [27]. Thus modeling and sequence learning tasks, and are one of the most improving transformer performance has been in the focus of

ortant machine learning workloads today. Training one is a ery compute-intensive task, often taking days or weeks, and mificant attention has been given to optimizing transform Despite this, existing implementations do not efficiently utilize GPUs. We find that data movement is the key bottleneck when training. Due to Amdahl's Law and massive improvements in e performance, training has now become a Further, existing frameworks use suboptimal data layouts, Using these insights, we present a recipe for globally optimizing data novement in transformers. We reduce data movement by up to 22.91% and overall achieve a 1.30× perform ment over state-of-the-art frameworks when training BERT. approach is applicable more broadly to optimizing deep

numerous research and industrial groups Significant attention has been given to optimizing transform ers: local and fixed-window attention [28]-[32], more general structured sparsity [33], learned sparsity [34]-[36], and other algorithmic techniques [19], [37] improve the performance of ransformers. Major hardware efforts, such as Tensor Cores and TPUs [38] have accelerated tensor operations like matrix matrix multiplication (MMM), a core transformer operation Despite this, existing implementations do not efficiently utilize GPUs. Even optimized implementations such as Mega iral networks, and offers insight into how to tackle emerging

#### tron [18] report achieving only 30% of peak GPU flop/s. We find that the key bottleneck when training transform-

- Use larger clusters (10k+ GPUs)
- Model parallelism
  - Complex pipeline schemes
- Sparsification

3	PARCML	for Machi	ine Learning	luncation	
Ced ET	ric Renggli TH Zurich	Saleh Ashkboos IST Austria	Mehdi Aghagolzadeh Microsoft	Dan Alistarh IST Austria	
		Torste	en Hoefler I Zurich		- 11
	Demysti	fying Parallel	and Distributed	Deep Learnin	g: An
	In-Depth	Concurrence	y Analysis	-	-
G] 15 Sep 2018	TAL BEN-NU Deep Neural Ne their training i design. In this its parallelizati strategies. We t through paralle stochastic optin search. Based o CCS Concepts: networks; Par	UN and TORSTENT + tworks (DNNs) are beco is a major challenge an survey, we describe the on. We present trends in then review and model 1 lism in network inferene mization, distributed sys on those approaches, we • General and reference allel computing meth	TOEFLEK, ETH Zurich, Swit ming an important tool in mode d techniques range from distr problem from a theoretical pi DNN architectures and the re- hehe different types of concurre re and training, to distributed stem architectures, communic extrapolate potential direction <i>e o Surveys and overviews</i> . C toolologies; Distributed com	zerland rn computing application rhuted algorithms to lo respective, followed by a sulting implications on ney in DNNs: from the s leep learning. We discuss ation schemes, and neur so for parallelism in deep omputing methodolog puting methodologies	as. Acceleratin w-level circu approaches fo parallelizatio single operato s asynchronou al architectur > learning. gies → Neura



# High-Performance I/O for deep learning

Nail

- Example: ResNet-50 3.8 Gflop inference,  $\approx$  3x for training
  - ImageNet is 150 GiB for  $\approx$ 1.3M images  $\rightarrow$  average size 115 kiB, range: 508B 15MiB
  - MLPerf on one A100 2.9k samples/s  $\rightarrow$  333 MiB/s random access  $\rightarrow$  2 SSDs / GPU 2-4x that for scientific problem such as CosmoFlow
- Training on thousands of GPUs may need to manage  $k \times 1000s$  of SSDs



- But why do we need those even? Deep Learning workloads "randomly sample" input!
  - By "random", we really mean pseudo-random sequences with fixed seeds 😳

This enables clairvoyant prefetching!





### Clairvoyant Prefetching for Distributed Machine Learning I/O (arXiv 2101.08734) NoPFS acts as a distributed cache – each node keeps cache – fully knowing about the future! PRNG seed $\rightarrow$ Access stream $R = (\cdots, 7, 4, 5, 8, \cdots)$ single-process access to samples for ImagerNet with 16 processes Accesses for worker *i* Cached in local amples 200000 storage Fetched from remote 150000 workers Samples Some Access Frequency samples are Filled in access order R 100000 accessed 18 Storage class 2 Faster times! Storage class 1 50000 pending framework get used 0 Staging buffer 5 4 8 2 12 14 16 18 0 10 Access frequency

The second of



# Clairvoyant Prefetching for Distributed Machine Learning I/O (arXiv 2101.08734)

NoPFS acts as a distributed cache – each node keeps cache – fully knowing about the future!





# Clairvoyant Prefetching for Distributed Machine Learning I/O (arXiv 2101.08734)

NoPFS acts as a distributed cache – each node keeps cache – fully knowing about the future!





## runtime per epoch (full training time)



ImageNet 1k with ResNet-50



## Pushing the envelope in large-scale learning





**High-Performance I/O** 

- Quickly growing data volumes
  - Scientific computing!
- Use the specifics of machine learning workloads
  - E.g., intelligent prefetching

CLAIRVOYANT PREFETCHING FOR DISTRIBUTED MACHINE LEARNING I/O

Roman Böhringer<sup>1</sup> Nikoli Dryden<sup>1</sup> Tal Ben-Nun<sup>1</sup> Torsten Hoefler<sup>1</sup>

#### BSTRACT

I/O is emerging as a major bottleneck for machine learning training, especially in distributed environments such as clouds and supercomputers. Optimal data ingestion pipelines different between systems, and increasing efficiency requires a delicate balance between access to local storage, external filesystems, and remote workers; yet existing frameworks fail to efficiently utilize such resources. We observe that, given the seed generating the random access pattern for training with SGD, we have *clairvoyance* and can exactly predict when a given sample will be accessed. We combine this with a theoretical analysis of access patterns in training and performance modeling to produce a novel machine learning I/O middleware, HDMLP, to tackle the I/O bottleneck. HDMLP, provides an easy-to-use, flexible, and scalable solution that delivers better performance than state-of-the-art approaches while requiring very few changes to existing codebases and supporting a broad range of environments.

### **High-Performance Compute**

- Deep learning is HPC
  - Same major problems
  - Data movement!

### Data Movement Is All You Need: A Case Study on Optimizing Transformers Andrei Ivanov\*, Nikoli Dryden\*, Tal Ben-Nun, Shigang Li, Torsten Hoefler ETH Zurich firstname 1.lastname@infi.ethz.ch \* Equal contribution

0 very compute-intensive task, often taking days or vereks, and misginfloar attention has been given to optimizing transformers. Despite this, estisting implementations do not efficiently utilize of GPUs. We find that data movement is the key bottleneck when a significant takes the set of the

roach is applicable more broadly to

enset is a moving transformer performance has been in the focus of s, and s, and the set of the set of the focus of s, and s,

#### the subplicable more broadly to optimizing deep prks, and offers insight into how to tackle emerging bottlenecks. We find that the key bottleneck when training transform

- Use larger clusters (10k+ GPUs)
- Model parallelism
  - Complex pipeline schemes
- Sparsification

5	SparCML	: High-Perform for Machi	ance Sparse Comm ne Learning	nunication	
Ce E	dric Renggli TH Zurich	Saleh Ashkboos IST Austria	Mehdi Aghagolzadeh Microsoft	Dan Alistarh IST Austria	
		Torste	n Hoefler		
	Demysti	fying Parallel	and Distributed	Deep Learnin	g: An
	In-Depth	Concurrency	/ Analysis	-	
G] 15 Sep 2018	TAL BEN-NU Deep Neural Ne their training i design. In this ist parallelizati strategies. We t through paralle stochastic optin search. Based o CCS Concepts: n networks; Par	JN and TORSTEN H tworks (DNNs) are becor is a major challenge and survey, we describe the on. We present trends in then review and model 1 lism in network inference inization, distributed sys in those approaches, we General and reference allel computing meth	HOEFLER, ETH Zurich, Swit ming an important tool in model d techniques range from dist problem from a theoretical p DNN architectures and the re- he different types of concurre re and training, to distributed d tem architectures, communic extrapolate potential direction $\mathbf{e} \rightarrow Surveys$ and overviews; - C odologies; Distributed com	zerland rn computing application thuted algorithms to lo erspective, followed by a sulling implications on ney in DNNs; from the s leep learning. We discuss those the second secon	as. Acceleratin, w-level circui upproaches fo parallelization ingle operato a saynchronou al architectur o learning. cies → Neura ;



## Data Movement Is All You Need: A Case Study on Optimizing Transformers (arXiv:2007.00072)



Last week, OpenAI published a paper detailing GPT-3, a machine learning model that achieves strong results on a number of natural language benchmarks. At 175 billion parameters, where a parameter affects data's prominence in an overall prediction, it's the largest of its kind. And with a memory size exceeding 350GB, it's one of the priciest, costing an estimated \$12 million to train.

	highly				
Operator class	optimized	% flo	р	% Rur	ntime
Tensor contraction		99.80		61.0	
Statistical normalization		0.17		25.5	
Element-wise		0.03		13.5	
		0.2%		39%	

## **Our performance improvement for BERT-large**

- 30% over PyTorch
- 20% over Tensorflow + XLA
- 8% over DeepSpeed

est. savings on AWS over PyTorch: \$85k for BERT, \$3.6M GPT-3

## Data Movement Is All You Need: A Case Study on Optimizing Transformers (arXiv:2007.00072)





## Pushing the envelope in large-scale learning





**High-Performance I/O** 

- Quickly growing data volumes
  - Scientific computing!
- Use the specifics of machine learning workloads
  - E.g., intelligent prefetching

CLAIRVOYANT PREFETCHING FOR DISTRIBUTED MACHINE LEARNING I/O

Roman Böhringer<sup>+</sup> Nikoli Dryden<sup>+</sup> Tal Ben-Nun<sup>+</sup> Torsten Hoefler<sup>+</sup>

#### BSTRACT

I/O is emerging as a major bottleneck for machine learning training, especially in distributed environments such as clouds and supercomputers. Optimal data ingestion pipelines differ between systems, and increasing efficiency requires a delicate balance between access to local storage, external filesystems, and remote workers; yet existing frameworks fail to efficiently utilize such resources. We observe that, given the seed generating the random access pattern for training with SGD, we have *clairvoyance* and can exactly predict when a given sample will be accessed. We combine this with a theoretical analysis of access patterns in training and performance modeling to produce a novel machine learning I/O middleware, HDMLP, to tackle the I/O bottleneck. HDMLP provides an easy-to-use, flexible, and scalable solution that delivers better performance than state-of-the-art approaches while requiring very few changes to existing codebases and supporting a broad range of environments. **High-Performance Compute** 

- Deep learning is HPC
  - Same major problems
  - Data movement!

### Data Movement Is All You Need: A Case Study on Optimizing Transformers Andrei Ivanov<sup>\*</sup>, Nikoli Dryden<sup>\*</sup>, Tal Ben-Nun, Shigang Li, Torsten Hoeffer ETH Zurich firstnamelistin, ethr.eh

 
Abstract
Transformers have become widely used for language

modeling and sequence learning tworkloads today. Training one is a very compute intensive task, often tasking days or weeks, and significant attention has been given to optimizing transformers. Provide the custing implementation the one technicative utility training. Due to Annahly. Law and massive improvements in compute performance, training has now become memory-bound. Further, existing frameworks use suboptimal data hayouts. Using these insights, we present a relief per globally optimizing data movement in transformers. May estimate tensor op to 22:01% the art over a L30% performance improve to 12:201% and overall abiles are low become training IDERT. Our approach is applicable mark banck banche and training MERT.
cplicable and training the provide training the provide tensor op training the set of the set of the provide tensor op to 22:01% to set of the art frameworks when training IDERT. Our approach is applicable more broady to optimizing data
cplicable art frameworks optimized the training MERT.

Our approach is applicable and the applicable training the set of the set

- Use larger clusters (10k+ GPUs)
- Model parallelism
  - Complex pipeline schemes
- Sparsification

S	PARCML	: High-Perform for Machi	ance Sparse Comm ne Learning	unication	
Cec E	lric Renggli FH Zurich	Saleh Ashkboos IST Austria	Mehdi Aghagolzadeh Microsoft	Dan Alistarh IST Austria	
		Torste	n Hoefler		
	Demysti	fying Parallel	and Distributed	Deep Learnin	g: An
	In-Depth	Concurrency	' Analysis		
G] 15 Sep 2018	TAL BEN-NU Deep Neural Ne their training design. In this its parallelizati strategies. We through paralle stochastic optin search. Based o CCS Concepts: networks; Par	JN and TORSTEN H tworks (DNNs) are becon is a major challenge ann survey, we describe the on. We present trends in then review and model t ilsm in network inferenc mization, distributed sys ilsm in those approaches, we - General and reference allel computing meth	IOEFLER, ETH Zurich, Switi ning an important tool in model It techniques range from dists problem from a theoretical po DNN architectures and the ro- he different types of concurre e and training, to distributed d tem architectures, communic extrapolate potential direction e — Surveys and overviews. C odologies; Distributed com	zerland rn computing application ibuted algorithms to lo esulting implications on ncy in DNNs: from the s teep learning. We discuss ation schemes, and neur us for parallelism in deep omputing methodolog puting methodologies	s. Acceleratin w-level circui upproaches fo parallelization ingle operato asynchronou al architectur learning. ies → Neura



# The three dimensions of parallelism in deep learning (arXiv:1802.09941)



- Large-scale deep learning will need all three dimensions!
  - Depends on the exact model configuration
  - Sparsity makes it much more complex (interesting, more later)!

## Data-parallel gradient sparsification – top-k SGD (arXiv:1809.10505)

- Turns out 90-99.9% of the gradient values can be skipped by choosing the top-k achieve similar accuracy
  - Accumulate the remainder locally (convergence proof, similar to async. SGD with implicit staleness bounds [1])

**Assumption 1.** There exists a (small) constant  $\xi$  such that, for every iteration  $t \ge 0$ , we have:

$$\left\| \operatorname{TopK}\left(\frac{1}{P}\sum_{p=1}^{P} \left(\alpha \tilde{G}_{t}^{p}(v_{t}) + \epsilon_{t}^{p}\right)\right) - \sum_{p=1}^{P} \frac{1}{P} \operatorname{TopK}\left(\alpha \tilde{G}_{t}^{p}(v_{t}) + \epsilon_{t}^{p}\right) \right\| \leq \xi \|\alpha \tilde{G}_{t}(v_{t})\|.$$

**Discussion.** We validate Assumption 1 experimentally on a number of different learning tasks in Section 6 (see also Figure 1). In addition, we emphasize the following points:



[1] Dan Alistarh, TH, et al.: "The Convergence of Sparsified Gradient Methods", NIPS'18



# SparCML – Sparse allreduce for decentral updates (arXiv:1802.08021)









## Microsoft Speech Production Workload Results – 2 weeks → 2 days!

System	Dataset	Model	# of nodes	Algorithm	Speedup
Piz Daint	ImageNet	VGG19	8	Q4	1.55 (3.31)
Piz Daint	ImageNet	AlexNet	16	Q4	1.30 (1.36)
Piz Daint EC2	MNIST	MLP	8	Top16_Q4 Top16_Q4	3.65 (4.53) 19.12 (22.97)

# Unbalanced workloads in deep learning $\rightarrow$ eager-SGD (arXiv:1908.04207)

Some training scenarios cause different work across examples – e.g., video inputs of different lengths





## Unbalanced workloads in deep learning $\rightarrow$ eager-SGD (arXiv:1908.04207)



Top-1 test accuracy and runtime for LSTM on UCF101 using 8 GPUs.



eager SGD	first triggers (solo)	majority triggers
Speedup (over Horovod)	1.64x	<b>1.27</b> x
Тор-1	average: 60.6%	average: 69.7%
(test accuracy)	(up to 70.4%)	(up to 72.8%)



# Next step – wait-avoiding grouped model averaging (WAGMA) (arXiv:2005.00124)

Model averaging sums the model weights, not the gradients – can delay summation even more

Idea: sum partial groups, e.g., stages of allreduce for log<sub>2</sub> n groups





# **Onwards to the future of large-scale learning and scientific computing!**





## **High-Performance I/O**

- Quickly growing data volumes
  - Scientific computing!
- Use the specifics of machine learning workloads
  - E.g., intelligent prefetching

**CLAIRVOYANT PREFETCHING FOR DISTRIBUTED MACHINE LEARNING I/O** 

Roman Böhringer Nikoli Dryden<sup>1</sup> Tal Ben-Nun<sup>1</sup> Torsten Hoefle

### ABSTRACT

I/O is emerging as a major bottleneck for machine learning training, especially in distributed environments such as clouds and supercomputers. Optimal data ingestion pipelines differ between systems, and increasing efficiency requires a delicate balance between access to local storage, external filesystems, and remote workers; yet existing frameworks fail to efficiently utilize such resources. We observe that, given the seed generating the random access pattern for training with SGD, we have clairvoyance and can exactly predict when a given sample will be accessed. We combine this with a theoretical analysis of access patterns in training and performance modeling to produce a novel machine learning I/O middleware, HDMLP, to tackle the I/O bottleneck. HDMLP provides an easy-to-use, flexible, and scalable solution that delivers better performance than state-of-the-art approaches while requiring very few changes to existing codebases and supporting a broad range of environments

### **High-Performance Compute**

- Deep learning is HPC
  - Same major problems
  - Data movement!

#### Data Movement Is All You Need: A Case Study on **Optimizing Transformers** Andrei Ivanov\*, Nikoli Dryden\*, Tal Ben-Nun, Shigang Li, Torsten Hoefler ETH Zürich firstname.lastname@inf.ethz.ch Equal contribution Abstract-Transformers have become widely used for language challenges such as artificial general intelligence [27]. Thus nodeling and sequence learning tasks, and are one of the most improving transformer performance has been in the focus of rtant machine learning workloads today. Training one is a

ry compute-intensive task, often taking days or weeks, and nificant attention has been given to optimizing transform spite this, existing implementations do not efficiently utilize GPUs. We find that data movement is the key bottleneck wher training. Due to Amdahl's Law and massive improvements in e performance, training has now become Further, existing frameworks use suboptimal data layouts, Using these insights, we present a recipe for globally optimizing data novement in transformers. We reduce data movement by up to 22.91% and overall achieve a 1.30× perfor ment over state-of-the-art frameworks when training BERT.

approach is applicable more broadly to

numerous research and industrial groups Significant attention has been given to optimizing transform ers: local and fixed-window attention [28]-[32], more general structured sparsity [33], learned sparsity [34]-[36], and other algorithmic techniques [19], [37] improve the performance of ransformers. Major hardware efforts, such as Tensor Cores and TPUs [38] have accelerated tensor operations like matrix matrix multiplication (MMM), a core transformer operation Despite this, existing implementations do not efficiently utilize GPUs. Even optimized implementations such as Mega tworks, and offers insight into how to tackle emerging

#### tron [18] report achieving only 30% of peak GPU flop/s. We find that the key bottleneck when training transform-

- Use larger clusters (10k+ GPUs)
- Model parallelism
  - Complex pipeline schemes
- Sparsification

Ce E	lric Renggli TH Zurich	Saleh Ashkboos IST Austria	Mehdi Aghagolzadeh Microsoft	Dan Alistarh IST Austria	
		Torste	en Hoefler I Zurich		- 10
	Demysti	fying Parallel	and Distributed	Deep Learnin	g: An
	In-Dept	h Concurrency	y Analysis	-	-
] 15 Sep 2018	Deep Neural Ne their training design. In this its parallelizati strategies. We through paralle stochastic opti search. Based of CCS Concepts: networks; Par	etworks (DNNs) are becore is a major challenge an survey, we describe the ion. We present trends in then review and model 1 elism in network inferene imization, distributed sy: on those approaches, we • General and reference rallel computing meth	ming an important tool in mode d techniques range from dist problem from a theoretical per to DNN architectures and the re- he different types of concurre; e and training; to distributed stem architectures, communica extrapolate potential direction $\mathbf{e} \rightarrow Surveys$ and overviews; C <b>codologies</b> ; <b>Distributed</b> com	rn computing application ibuted algorithms to le respective, followed by sulting implications on ncy in DNNs: from the : eep learning, We discuss ation schemes, and neue is for parallelism in deep omputing methodologie puting methodologie	ns. Acceleratin ww-level circu approaches fo parallelizatio single operato s asynchronou ral architectur p learning. gies → Neura s;