

Virtual ModSim 2021 – Day One, Tuesday October 5, 2021	
Start 11:00AM Eastern Daylight Time (US-EDT)	
<i>Introductions and Keynote Speaker</i>	
11:00-11:15 am	Introduction to the 2021 ModSim Workshop – <b>Adolfy Hoisie</b>
11:15am-12:00 pm	<b>Keynote Speaker:</b> Victoria Coleman - <b>Digital Engineering in support of the Department of the Air Force Mission</b>
<i>Session Title Machine Learning for ModSim – Session Lead: Noel Wheeler</i>	
12:00-12:30 pm	Adolfy Hoisie – <b>Computer Architecture Simulation Using Machine Learning</b>
12:30-1:00 pm	Rick Stevens - <b>The Development and Performance of AI Driven Surrogates in COVID-19 Therapeutic Small Molecule Design Workflows</b>
1:00-1:30 pm	Lizhong Chen – <b>Enabling Large Architectural Design Space Exploration Using Machine Learning</b>
1:30-1:40 pm	<i>10-minute Break</i>
<i>Session Title ModSim State-of-the-art: Methods, tools, projects – Session Lead: Almadena Chtchelkanova</i>	
1:40-2:10 p.m.	Felix Wolf – <b>The Price Performance of Performance Models</b>
2:10-2:40 p.m.	Jason Liu – <b>A Unified Framework for Performance Analysis and Optimization of Memory Systems</b>
2:40-3:10 p.m.	Matt Sinclair– <b>Modeling Modern GPU Applications in gem5</b>
3:10-3:20 p.m.	<i>Closing Remarks</i>
	<b>End of Day One</b>

<b>ModSim 2021 – Day Two, Wednesday October 6, 2021</b>	
<i>Start 11:00AM Eastern Daylight Time (US - EDT)</i>	
11:00-11:05 am	Opening Remarks Day 2 – <b>Adolfy Hoisie</b>
11:05-11:35 am	Luca Carloni – <b>ESP: an Open-Source Platform for Collaborative Design of Heterogeneous Systems</b>
11:35am -12:05 pm	Aurel Lazar – <b>FlyBrainLab: a Complete Programming Environment for Discovering the Functional Logic of the Fruit Fly Brain</b>
12:05-12:45pm	<b>Keynote Speaker:</b> Serge Leef - <b>Accelerating Simulation via AI-derived Reduced-Order Models</b>
<i>Session Title AI/ML Application Workload Characterization – Session Lead: Bruce Childers</i>	
12:45-1:15 pm	Torsten Hoefler – <b>High-Performance Scalable Deep Learning</b>
1:15-1:45 pm	Joe Cross – <b>ModSim as it applies to ERI</b>
1:45-2:15 pm.	David Kanter – <b>Challenges and Directions in ML System Performance: The MLPerf™ Story</b>
2:15-2:25 p.m.	<b>Closing Remarks</b>
	<b>End of Day Two</b>

<b>ModSim 2021 – Day Three, Thursday October 7, 2021</b>	
<i>Start 11:00AM Eastern Daylight Time (US-EDT)</i>	
11:00-11:10AM	Opening Remarks – Session Leader
<b><i>Contributed Presentations Session: – RAPID-FIRE : Session Leads: Martin Schulz and Pradip Bose</i></b>	
11:10-11:20a.m.	Tushar Krishna – <b>ASTRA-sim: Enabling SW/HW Co-Design Exploration for Distributed Deep Learning Training Platforms</b>
11:20-11:30a.m.	Bogil Kim – <b>NPUsim: Full-System, Cycle-Accurate, Function Simulations of Deep Neural Network Accelerators</b>
11:30-11:40a.m.	T. Patrick Xiao – <b>CrossSim: GPU-Accelerated Simulation of Analog Neural Networks</b>
11:40-11:50a.m.	Arun Rodrigues – <b>SST-Explorer: Enabling System-level Performance and Reliability Analysis for Designs with Real-World IPs</b>
11:50am-12:00pm	Geonhwa Jeong – <b>UNION: A HW-SW Co-Design Ecosystem in MLIR for Evaluating Tensor Operations on Spatial Accelerators</b>
<i>Poster Only</i>	<i>Gwen Voskuilen - Towards an Extensible Framework for Accelerated System Simulation</i>
<b>12:00-12:20pm</b>	<b><i>Group 1 Poster Session Q&amp;A – Breakout Rooms</i></b>
12:20-12:30p.m.	Daniel Mosse – <b>sing Occam for Reducing Search Space for Multiobjective Optimization through Machine Learning Models</b>
12:30-12:40p.m.	Mark Plagge – <b>ATHENA: A High Efficiency Codesign Tool for Novel Accelerators</b>
12:40-12:50p.m.	Jaewon Lee – <b>Trace Generation of Machine Learning Workloads with GTReplay For Intel integrated-GPU Modeling</b>
12:50-1:00p.m.	Subhankar Pal – <b>Fast Trace-Driven Simulation of Programmable Heterogeneous Accelerators</b>
1:00-1:10p.m.	Lingda Li – <b>Machine Learning Model Exploration for Accurate and Fast Microarchitecture Simulation</b>
<i>Poster Only</i>	<i>Jack Jones - SimEng: a fast, easy to use, open source processor simulation framework</i>
<b>1:10-1:30pm</b>	<b><i>Group 2 Poster Session – Breakout Rooms</i></b>
1:30-1:40p.m.	Bobby Bruce – <b>Democratizing Computer SystemSimulation with a Components Library</b>
1:40-1:50p.m.	Mariam Kiran – <b>Using AI for Self-Driving Networks</b>
1:50-2:00p.m.	Sophia Shao – <b>Enabling Holistic Machine-Learning Hardware Evaluation via Full-System Simulation</b>
2:00-2:10 p.m.	Thomas Flynn – <b>Design space-aware statistical simulation with machine learning</b>
2:10-2:20 p.m.	Jeffrey Young – <b>Accurately Modeling Sparse Accesses for Benchmarking and Architectural Simulation</b>
<i>Poster Only</i>	<i>Ayaz Akram - Simulating Trusted Execution Environments in gem5</i>
<b>2:20-2:40pm</b>	<b><i>Group 3 Poster Session – Breakout Rooms</i></b>
2:40-2:50 p.m.	<b>Closing Remarks – Session Leader</b>
	<b>End of Day Three</b>

<b>ModSim 2021– Day Four, Friday October 8, 2021</b>	
<b>Start 11:00AM Eastern Daylight Time (US-EDT)</b>	
11:00-11:15 a.m.	<i>Opening Remarks and Sudhakar Yalamanchili – White Paper <b>AWARD Presented</b> – Hyesoon Kim</i>
<b>Session Title AI Architectures, Advances in ModSim– Session Lead: Jason Lowe-Power</b>	
11:15-11:45 a.m.	<b>Rob Schreiber – Wafer-scale Processors for HPC</b>
11:45am-12:15pm	<b>Luis Ceze – Improving Model Performance, Portability and Productivity with Apache TVM and the Octomizer</b>
12:15-12:45 p.m.	<b>Nicholas Malaya –Accelerating Computational Fluid Dynamics with ML/AI at AMD</b>
12:45-12:50 p.m.	<b>Short 5-minute Break</b>
12:50 – 2:30 p.m.	<i>Panel: What can MODSIM do for AI/ML, and what can AI/ML do for MODSIM?: <u>Moderator</u>: Shekhar Borkar and Pradip Bose <u>Panelist</u>: David Kanter, Eric Cheng, Hal Finkel, Hyesoon Kim, Sophia Shao</i>
2:30 -2:45 p.m.	<b>Workshop Wrap-up</b>