## Attachment A

**ARM Digital Display/Controller and EPICS Interface Integration Test Data Sheet**

**Note:** A new Test Data Sheet shall be completed for each ARM Digital Display/Controller.

**Reason for Test:**
- Annual □
- New Installation ☑
- New Location □
- EEPROM Firmware Version Change □
- EPICS Communication Software Version Change □

**Version Tested:** 1.03B1

<table>
<thead>
<tr>
<th>Freq(Hz) / expected mR/h</th>
<th>HVP Width Set to &quot;0&quot;</th>
<th>DEVICE</th>
<th>EPICS</th>
<th>DEVICE</th>
<th>EPICS</th>
<th>DEVICE</th>
<th>EPICS</th>
<th>DEVICE</th>
<th>EPICS</th>
<th>DEVICE</th>
<th>EPICS</th>
<th>COMM.</th>
<th>FAIL after 60 sec</th>
<th>HVP Width Set to &quot;70&quot;</th>
</tr>
</thead>
<tbody>
<tr>
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<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>RED LED</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Serial #</td>
<td>EN007309</td>
<td>0.01</td>
<td>0.01</td>
<td>1.00</td>
<td>1.00</td>
<td>10.00</td>
<td>10.00</td>
<td>100.13</td>
<td>100.13</td>
<td>100.61</td>
<td>100.61</td>
<td>RED LED</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Date</td>
<td>12/22/2016</td>
<td>51 Hz</td>
<td>150 Hz</td>
<td>1,050 Hz</td>
<td>10,050 Hz</td>
<td>10,050 Hz</td>
<td>10,050 Hz</td>
<td>10,050 Hz</td>
<td>10,050 Hz</td>
<td>10,050 Hz</td>
<td>10,050 Hz</td>
<td>RED LED</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Location</td>
<td>FOR-J102</td>
<td>GREEN</td>
<td>GREEN</td>
<td>GREEN</td>
<td>RED</td>
<td>RED</td>
<td>RED</td>
<td>RED</td>
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<td>RED</td>
<td>RED</td>
<td>RED LED</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Accelerator Safety Systems Staff (Tester) Name:** Stephen Kowalski

**Signature:** [Signature Image]

**Date:** 12/22/2016

**RCD Staff Name:** Shawn Buckmaster

**Signature:** [Signature Image]

**Date:** 12/22/2016

**RCD Supervisor Name:** John Ack

**Signature:** [Signature Image]

**Date:** 12/22/2016